BACHELOR OF TECHNOLOGY (C.B.C.S.) (2021-COURSE) B. Tech. Sem - II CS&BS :SUMMER- 2022 SUBJECT : PRINCIPLES OF ELECTRONICS ENGINEERING

: Frida :: 5/8/2	*	Time: 10:00 AM-01: S-24140-2022 Max. Marks: 60	00 PN
N.B.	 All Questions are COMPULSORY. Figures to the right indicate FULL marks. Use of non – programmable calculator is ALLOWED Assume suitable data WHEREVER necessary. Draw neat labeled diagrams WHEREVER necessary. 		
Q. 1	Describe the Fermi levels for intrinsic semiconductor, n-type extrinsic (1 semiconductor and p-type extrinsic semiconductor with the help of energy band diagram.		
	,	Differentiate between extrinsic semiconductor and intrinsic semiconductor. Describe the concept of majority and minority charge carriers in extrinsic semiconductors.	(0¢
Q. 2	Draw and explain full wave Rectifier with center tap transformer with the help of waveforms. State the advantages of full wave rectifier. OR		
	Describe the effect of forward and reverse biasing on the width of depletion region on P-N junction diode. Also Draw the V-I characteristic graph of Ideal diode and practical diode.		
Q. 3	Describe the operation of NPN Transistor in Active region with suitable diagram. Also comment on dopping concentration and area allocation for Emitter, Base and Collector.		
		OR ine stability factor s, s' and s". Derive the general expression for stability factor ransistors.	(10
Q. 4	Draw and explain operation of p-channel JEET with Drain and Transfer characteristic graph.		
		OR Describe basic working principle of CMOS. List the applications of FETs.	(06 (04
Q. 5	Des	cribe all topologies of feedback amplifier with block diagram. OR	(10
		Draw and explain op-Amp as a Adder. Explain the block diagram of Op-Amp.	(06 (04
Q. 6	Describe operation of basic gates and universal gates with the help of symbol, truth table and logic equations.		
	,	OR Reduce the expression, $\sum m (1,5,6,12,13,14) + d(2,4)$ Show, $A + \overline{A}B = A + B$	(06 (04

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