

MASTER OF TECHNOLOGY (ELECTRONICS - VLSI) (CBCS - 2015 COURSE)
M. Tech. (Electronics - VLSI) Sem-IV :SUMMER- 2022
SUBJECT : SELF-STUDY PAPER-II:COMPUTER AIDED VLSI DESIGN

Day : Wednesday

Time : 10:00 AM-01:00 PM

Date : 15-06-2022

S-14540-2022

Max. Marks : 60

N. B. :

- 1) All questions are **COMPULSORY**.
- 2) Figures to the right indicate **FULL** marks.
- 3) Draw neat and labeled diagram **WHEREVER** necessary.
- 4) Answers to both the sections should be written in **SEPARATE** answer books.
- 5) Assume suitable data, if necessary.

SECTION – I

Q. 1 How will you define design, hierarchy and views? Explain in brief. (10)

OR

Describe connectivity and spatial dimensionality. (10)

Q. 2 How will you define connectivity representation? Describe in detail. (10)

OR

What is view representation? Explain in detail. (10)

Q. 3 What are the synthesis tools? Give brief overview. (10)

OR

What is the role of post layout generators? Discuss in detail. (10)

SECTION – II

Q. 4 What is the role of geometrical rule checker? Describe in detail. (10)

OR

What are the dynamic analysis tools? Explain any one. (10)

Q. 5 How will you define declarative programming? Discuss in detail. (10)

OR

Discuss outputs of design aids in brief. (10)

Q. 6 Discuss input devices with reference to graphics engineering. (10)

OR

Explain command language feedback. (10)

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