

BACHELOR OF TECHNOLOGY (C.B.C.S.) (2014 COURSE)
B.Tech.Sem - VIII E &TC :SUMMER- 2022
SUBJECT : SYSTEM ON CHIP

Day : Wednesday
Date : 22-06-2022

S-13380-2022

Time : 02:30 PM-05:30 PM
Max. Marks : 60

N. B. :

- 1) All questions are **COMPULSORY**.
 - 2) Figures to the right indicate **FULL** marks.
 - 3) Draw neat and labelled diagrams **WHEREVER** necessary.
 - 4) Assume suitable data, if necessary.
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Q. 1 What is wrong with today's approach to SOC design? Give brief overview. **(10)**

OR

Q. 1 With reference to SOC design, how will you describe an improved design methodology? **(10)**

Q. 2 Discuss hardware system architecture using suitable block diagram. **(10)**

OR

Q. 2 Using suitable diagram, describe current SOC design flow. **(10)**

Q. 3 How will you describe configurable processor as RTL alternative? **(10)**

OR

Q. 3 Describe system design with suitable processors. **(10)**

Q. 4 Explain communication design w.r.t. SOC design. **(10)**

OR

Q. 4 What are the hardware interconnect mechanisms? Discuss w.r.t. SOC design. **(10)**

Q. 5 How will you optimize processors to match hardware? **(10)**

OR

Q. 5 Describe multiple processor debug and trace. **(10)**

Q. 6 What are the applications of complex SOC? **(10)**

OR

Q. 6 What is happening to SOC design? **(10)**

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