

S.D.E.

M.C.A. SEM - V : WINTER - 2017

SUBJECT: I.T, ELECTIVE- III EMBEDDED SYSTEM INTERFACING & EMBEDDED  
COMMUNICATION SYSTEM

Day : **Thursday**  
Date : **14/12/2017**

Time : **10.00 A.M. TO 1.00 P.M.**  
Max Marks : 80

**W-2017-4442**

N.B. :

- 1) Attempt any **FIVE** questions from Section – I and any **TWO** questions from Section – II
- 2) Figures to the right indicate **FULL** Marks.
- 3) Answers to both the section should be written in **SEPARATE** answer book.

**SECTION - I**

- Q.1** Explain ADC interfacing in detail. (10)
- Q.2** What are various hardware interrupts. Explain any two in detail. (10)
- Q.3** Explain RS 232 PC to PC interfacing in detail. (10)
- Q.4** Explain Serial & parallel communication in detail. (10)
- Q.5** Explain in detail servo motor interfacing with neat diagram. (10)
- Q.6** Explain in detail interrupt vector table. (10)
- Q.7** Write Short notes (Any Two) (10)
- a) TCP/IP protocol stack
  - b) Communication protocols
  - c) System integration

**SECTION - II**

- Q.8** Explain keyboard interfacing circuit with neat diagram. (15)
- Q.9** Explain LAN Communication interfacing with its major block functionality in detail. (15)
- Q.10** Draw interfacing diagram for USB Communication in detail. (15)

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