

**B.TECH. SEM -VII INFO. TECH. 2014 COURSE (CBCS) : WINTER -  
2017**

**SUBJECT : COMPILER CONSTRUCTION AND DESIGN**

Day : Friday  
Date : 12/01/2018

**W-2017-2302**

Time : 02.30 PM TO 05.30 PM  
Max. Marks : 60

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**N. B. :**

- 1) All questions are **COMPULSORY**.
  - 2) Figures to the right indicate **FULL** marks.
  - 3) Assume suitable data if necessary.
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**Q. 1** Explain in brief 'Phases of Compiler'. (10)

**OR**

Enlist and explain 'Design issues in Compiler Construction'. (10)

**Q. 2** What are the issues in Top-Down parsing? (10)

**OR**

With block schematic explain working of SLR parser. (10)

**Q. 3** Explain inherited attribute with suitable example. (10)

**OR**

Comment on 'Every S-attributed definition is L-attributed'. (10)

**Q. 4** Describe various storage allocation strategies. (10)

**OR**

Explain with example: Symbol Tables. (10)

**Q. 5** How machine independent optimization is achieved? (10)

**OR**

Write a short note on 'Issues in Code Generation'. (10)

**Q. 6** Write a short note on: (10)

- i) Cross compiler
- ii) Decompiler

**OR**

Distinguish between Static and Dynamic compilation. (10)

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