

**B.SC. (I. T.) SEM. - II (CBCS - 2015 COURSE) : WINTER -
2017
SUBEJCT: COMPUTER ARCHITECTURE**

Day: **Saturday**
Date: **23/12/2017**

W-2017-0846

Time: **02.30 pm to 05.30 pm**
Max. Marks: 60

N.B.:

- 1) Attempt any **SIX** questions out of **EIGHT**.
 - 2) Figures to the right indicate **FULL** marks.
 - 3) Draw neat, labelled diagrams **WHEREVER** necessary.
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|------------|---|-------------|
| Q.1 | Explain Cache Memory and different Page Replacement Algorithms. | (10) |
| Q.2 | Explain modern computer multi-core architecture, with suitable diagram. | (10) |
| Q.3 | Differentiate between Address bus and Data bus. Also list the characteristics of PCI Bus. | (10) |
| Q.4 | Explain Booting process in typical computer. Also explain warm boot and cold boot. | (10) |
| Q.5 | Explain different types of Operating Systems. | (10) |
| Q.6 | Explain the features and working of Switch, Router and Gateway. | (10) |
| Q.7 | With the help of neat, labelled diagram, explain the architecture of 8085 Microprocessor. | (10) |
| Q.8 | What is BIOS in a typical computer? Describe its primary functions. | (10) |

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