

S.D.E.

M.C.A. Sem - V : WINTER - 2018

SUBJECT : ELECTIVE – III: DESIGN OF LANGUAGE PROCESSORS

Day : Friday
Date : 30/11/2018

Time : 10.00 AM TO 1.00 PM
Max. Marks : 80

W-2018-4818

N.B.:

- 1) Attempt **ANY FIVE** questions from Section – I and **ANY TWO** questions from Section – II.
- 2) Answers to both the sections should be written in **SEPARATE** answer books.
- 3) Figures to the right indicate **FULL** marks.

SECTION – I

- Q.1** Explain the concept of Deterministic Finite State Automata. **[10]**
- Q.2** What is LL – Grammar? **[10]**
- Q.3** Discuss the concept of Loading in brief. **[10]**
- Q.4** Discuss the concept of Structured Language in brief. **[10]**
- Q.5** What is Code Optimization and why it is required? **[10]**
- Q.6** What is debugging and how it is performed (debugging techniques)? **[10]**
- Q.7** Write short notes on **ANY TWO** of the following: **[10]**
- a) Code Generation
 - b) Two-Pass Assembler
 - c) Static Storage Allocation

SECTION – II

- Q.8** What is Intermediate Code? Discuss in detail. **[15]**
- Q.9** What is an Editor? Explain the various design and implementation issues related to them. **[15]**
- Q.10** Discuss the following terms with respect to an Assembler: **[08]**
- a) Op – Code Table **[08]**
 - b) Pass Structure **[07]**

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