

S.D.E.
B.C.A. (2004 Course Sem- III : WINTER - 2018
SUBJECT : COMPUTER ARCHITECTURE & OPERATING SYSTEM

Day Wednesday
Date 05/12/2018

Time 02.00 PM TO 05.00 PM
Max. Marks : 80

W-2018-4512

N.B.

- 1) Attempt any **FIVE** questions from Section – I and any **TWO** questions from Section – II.
- 2) Figures to the right indicate **FULL** marks.
- 3) Answers to both the sections should be written in **SEPARATE** answer book.

SECTION – I

- Q.1** Explain DMA operation in detail. (10)
- Q.2** With a example explain different types of addressing modes. (10)
- Q.3** Explain hard wired and micro programmed control unit. (10)
- Q.4** What are different modes of data transfer? Explain asynchronous data transfer in detail. (10)
- Q.5** With neat block diagram explain CPU control register organization. (10)
- Q.6** Describe memory management process without swapping with neat diagram. (10)
- Q.7** Write short notes on any **TWO**: (10)
- a) Virtual memory
 - b) Stack
 - c) Page replacement algorithm

SECTION – II

- Q.8** Define operating system. Describe various functions performed by operating system. (15)
- Q.9** Describe process scheduling algorithm with example. (15)
- Q.10** With suitable diagram explain various peripheral devices and their function. (15)

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