

M. Tech.-I (Information Technology) (CBCS – 2015 Course) :
WINTER - 2018

SUBJECT : PARALLEL PROGRAMMING & ALGORITHMS

Day : Monday
Date : 10/12/2018

W-2018-3119

Time : 11.00 AM TO 02.00 PM
Max. Marks : 60

N.B.:

- 1) All questions are **COMPULSORY**.
 - 2) Figures to the right indicate **FULL** marks.
 - 3) Answers to both the sections should be written in **SEPARATE** answer books.
 - 4) Assume suitable data if necessary.
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SECTION – I

Q.1 Describe Flynn's classification of parallel processing with neat diagrams. **[10]**

OR

Describe the parameters used to measure the performance of a pipeline.

Q.2 Explain the multistage interconnection network with neat diagrams. **[10]**

OR

Explain the loosely coupled multiprocessor system with neat diagram.

Q.3 Explain the concept of cluster computing in detail. **[10]**

OR

With neat diagram, describe the supercomputer architecture.

SECTION – II

Q.4 With neat diagram, explain the systolic array architecture. **[10]**

OR

Explain the concept of data flow computing in detail.

Q.5 Define parallelism. Explain the different types of parallelism. **[10]**

OR

What are different types of parallel programming models? Explain in brief.

Q.6 What are the different tools for parallel programming? Explain. **[10]**

OR

What are the characteristic features of VHDL? Explain.

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