

S.D.E.

B.C.A. (2004 COURSE SEM- VI : SUMMER - 2018

SUBJECT: MICROCOMPUTER & MAINTENANCE

Day: **Wednesday**
Date: **30/05/2018** **S-2018-4368**

Time: **10.00 AM TO 1.00 PM**
Max Marks: **80**

N.B:

- 1) Attempt any **FIVE** questions from Section-I.
- 2) Attempt any **TWO** questions from Section-II.
- 3) Figures to the right indicate **FULL** marks.

Answers to both the sections should be written in the **SEPARATE** answer books

SECTION-I

- Q.1 Explain in detail components of a digital computer. (10)
- Q.2 Explain the following (10)
- a) DMA Logic
 - b) Wait state logic
- Q.3 State the design variations of SMPS. (10)
- Q.4 Explain the working principle of CRT controller. (10)
- Q.5 Write a detailed note on 'Management of internet accounts' (10)
- Q.6 What is meant by Hardware fault? Explain various types of hardware faults. (10)
- Q.7 Write short notes on **ANY TWO** of the following: (10)
- a) time of day(TOD) logic
 - b) FDD
 - c) Microprocessor

SECTION-II

- Q.8 Explain in detail installation procedure of MS-OFFICE 2010. (15)
- Q.9 Explain step by step installation procedure of UNIX Network operating system. (15)
- Q.10 Explain the process of peer to peer networking using TCP/IP. (15)

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