

S.D.E.

M.C.A. SEM - V : SUMMER - 2018

SUBJECT : ELECTIVE – III: DESIGN OF LANGUAGE PROCESSORS

Day : **Friday**

Time : **10.00 A.M. TO 1.00 P.M.**

Date : **01/06/2018**

S-2018-4630

Max. Marks : 80

N.B.:

- 1) Attempt **ANY FIVE** questions from Section – I and **ANY TWO** questions from Section – II.
- 2) Answers to both the sections should be written in **SEPARATE** answer books.
- 3) Figures to the right indicate **FULL** marks.

SECTION – I

- Q.1** Explain the concept of Non-Deterministic Finite State Automata (Non-DFA). [10]
- Q.2** What is Bottom-up Parsing? [10]
- Q.3** Differentiate between Static Linking and Dynamic Linking. [10]
- Q.4** Discuss the concept of Symbol Table in detail. [10]
- Q.5** What is code optimization? Discuss in detail. [10]
- Q.6** What is Run-time Storage Administration? Explain in detail. [10]
- Q.7** Write short notes on **ANY TWO** of the following: [10]
- a) Syntax Tree
 - b) Two – Pass Assembler
 - c) Partial Compilation

SECTION – II

- Q.8** What is Interpretive Language? Discuss in detail. [15]
- Q.9** What is an Editor? List various types of editor and explain them in brief. [15]
- Q.10** Discuss the following terms with respect to an Assembler: [08]
- a) Intermediate code [08]
 - b) Forward reference [07]

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