

**M. TECH.-IV (ELECTRONICS V.L.S.I.) (CBCS – 2015  
COURSE) : SUMMER - 2018**  
**SUBJECT : SELF – STUDY PAPER – II : COMPUTER AIDED VLSI DESIGN**

Day : **Tuesday**  
Date : **19/06/2018**

**S-2018-3086**

Time : **11.00 AM TO 02.00 PM**  
Max. Marks : 60

**N.B.:**

- 1) All questions are **COMPULSORY**.
- 2) Figures to the right indicate **FULL** marks.
- 3) Answers to both the sections should be written in **SEPARATE** answer books.
- 4) Assume suitable data if necessary.

**SECTION – I**

**Q.1** Describe data flow environment in detail. **[10]**

**OR**

Describe views with respect to structure, activity and behavior of circuit.

**Q.2** Describe geometry representation with respect to shape attribute, transformation attribute and graphical attribute. **[10]**

**OR**

Describe hierarchy representation in detail.

**Q.3** Describe mask graphics in detail. **[10]**

**OR**

Describe min-cut placement and bottom-up placement method.

**SECTION – II**

**Q.4** Describe power consumption and power estimation in MOS transistor. **[10]**

**OR**

Describe polygon-based design rule checking.

**Q.5** List structuring methods. Describe imperative code and graphics. **[10]**

**OR**

Describe electronic design interchange format in detail.

**Q.6** Describe patterned filling and color inversion with reference to hardcopy graphics. **[10]**

**OR**

Describe the algorithm for drawing spline curves and circular curves for modern raster display accelerators.

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