

**B.TECH. SEM -IV E & TC 2014 COURSE (CBCS) : SUMMER -
2018**

SUBJECT: LINEAR INTEGRATED CIRCUITS

Day: **Saturday**
Date: **02/06/2018**

S-2018-2315

Time: **10.00 AM TO 01.00 PM**
Max Marks : 60

N.B.:

- 1) All questions are **COMPULSORY**.
- 2) Figures to the right indicate **FULL** marks.
- 3) Use of non programmable **CALCULATOR** is allowed.
- 4) Assume suitable data if necessary.

Q1. Draw and explain block diagram of typical Operational amplifier. (10)

OR

Explain characteristic parameters of ideal & practical for operational amplifier. (10)

Q.2 Draw & derive expression for non-inverting Amplifier. Derive Voltage follower from non-inverting amplifier. (10)

OR

Draw and derive expression for adder. (10)

Q.3 Draw and explain precision full wave rectifier. (10)

OR

Draw and explain Schmitt trigger. (10)

Q.4 Explain frequency response of high pass filter with neat circuit diagram. (10)

OR

Draw a circuit diagram for square wave generator and explain its working. (10)

Q.5 Explain working of monostable multivibrator with neat circuit diagram. (10)

OR

Explain any application of phase locked loop(PLL) IC i.e. 565. (10)

Q6. Explain I to V converter with neat circuit diagram. (10)

OR

Explain counter-ramp type A to D converter. (10)

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