

**B.TECH. SEM -VI INFO. TECH. 2014 COURSE (CBCS) :**

**SUMMER - 2018**

**SUBJECT: COMPUTER ORGANIZATION AND ARCHITECTURE**

**Day: Friday**  
**Date: 08/06/2018**

**S-2018-2431**

**Time: 02.30 PM TO 05.30 PM**  
**Max. Marks: 60**

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**N.B:**

- 1) All questions are **COMPULSORY**.
  - 2) Figures to the right indicate **FULL** marks.
  - 3) Assume suitable data if **NECESSARY**.
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**Q.1** Explain the different features of 80386 DX processor in detail. **(10)**

**OR**

With suitable figure, explain the internal architecture of 80386 in detail.

**Q.2** Explain the detailed working of flash-memory in detail. **(10)**

**OR**

Explain DDR3 and DDR4 memory technology.

**Q.3** List and explain the microoperations for the instruction- ADD AL, BL **(10)**

**OR**

With neat diagram, explain the working of hardwired control unit.

**Q.4** Draw and explain the internal architecture of Pentium processor. **(10)**

**OR**

What are the different addressing modes of Pentium processor? Explain with suitable example.

**Q.5** Explain the loosely coupled multiprocessor architecture with neat figure. **(10)**

**OR**

Explain the feature of RISC architecture with suitable diagram.

**Q.6** How parallelism can be achieved in a uniprocessor system? Explain. **(10)**

**OR**

What are the different levels of parallelism? Explain.

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