SUBJECT : SELF – STUDY PAPER – II : COMPUTER AIDED VLSI DESIGN

Day	:	Thursday		Time :	11.00 AM TO 02.00 PM
Date	:	13/06/2019	**S-2019-3489**	Max. Marks	: 60

N. B. :

1) All questions are **COMPULSORY**.
2) Figures to the right indicate **FULL** marks.
3) Draw neat and labeled diagram **WHEREVER** necessary.
4) Answer to both the sections should be written in **SAME** Answer book.
5) Assume suitable data, if necessary.

SECTION – I

Q. 1   How will you define design environment? Give brief overview.   **(10)**

OR

Describe connectivity and spatial dimensionality.   **(10)**

Q. 2   What are general issues of representation? Explain in brief.   **(10)**

OR

What is hierarchy representation? Discuss in detail.   **(10)**

Q. 3   Discuss cell contents generation.   **(10)**

OR

What is the role of silicon compiler? Explain in detail.   **(10)**

SECTION – II

Q. 4   What is the role of circuit level simulators? Explain in brief.   **(10)**

OR

Discuss event driven and hardware simulation.   **(10)**

Q. 5   Explain imperative programming using suitable example.   **(10)**

OR

What are the implementation issues with reference to design? Explain in detail.   **(10)**

Q. 6   Describe task and user modeling.   **(10)**

OR

Explain command language feedback.   **(10)**

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