## M. Tech.-I (Electrical -Power System) (CBCS – 2015 Course) : SUMMER - 2019 SUBJECT: FACTS AND HVDC

Day: Saturday Time: 11.00 AM TO 02.00 PM Date: 18/05/2019 Max Marks: 60 S-2019-3390 N.B.: All questions are COMPULSORY. 1) Figures to the right indicate FULL marks. 2) 3) Answer to both the sections should be written in **SAME** Answer book. 4) Use of non-programmable **CALCULATOR** is allowed. 5) Draw neat and labeled diagram WHEREVER necessary. 6) Assume suitable data if necessary. **SECTION-I** Q.1 Describe following terms for power flow in AC transmission system. (10)i) transient stability ii) voltage stability iii) power system oscillations OR Compare VSI and CSI fed converters. Discuss control issues in power (10) converters. Draw schematic diagram of FC-TCR and explain its principle of operation with (10) **Q.2** mathematical expression. Sketch its V-I characteristics. Draw schematic diagram of STATCOM and explain principle of operation. Compare SVC and STATCOM. Q.3 Draw UPFC configuration. What are the operational constraints of UPFC? (10)Explain the operation of UPFC when connected at sending end. (10)**SECTION-II Q.4** The HVDC link has the following parameters  $X_{cr} = X_{ci} = 28$  ohm,  $R_{dc} = 30$ ohm. The ac line voltage to the rectifier terminals is 320 kV when delivering 500 MW at 335kV DC. The inverter operates with an extinction angle at 21° Calculate. The delay angle of rectifier. i) ii) The ac line current and power factor at rectifier terminals. OR Explain the causes of reactive power absorbed by HVDC converter substation. Find DC output voltage of 12 pulse converter bridge with delay angle of 15<sup>0</sup> and overlap angle of 30°. AC line voltage is 300 KV with converter transformer ratio of 0.5. What are the advantages/ limitations of monopolar mode with ground return (10) Q.5 and monopolar mode with metallic return? Compare series and parallel type multi terminal HVDC system. Draw neat (10) sketch and state applications. Describe HVDC station control in lead station and trail station using block (10)**Q.6** diagram. OR What are the objectives of HVDC system control? Draw schematic diagram of (10)current control in two terminal HVDC system and explain its operation.