## IMAA - . SEM-II (2014 Course) CBCS : SUMMER - 2019 SUBJECT : COMPUTER ORGANIZATION & ARCHITECTURE

Time: 10.00 AM TO 01.00 PM Day : Monday S-2019-: 2117 Date Max. Marks: 100 15/04/2019 N.B. Answer any FOUR questions from Section - I and any TWO questions from 1) Section - II. 2) Figures to the right indicate FULL marks. Answers to both the sections should be written in SAME answer book. 3) **SECTION - I Q.1** Draw the block diagram of computer and explain each component in detail. (15)Explain 2 to 4 line decoder with NAND gates in detail. **Q.2** (15)Q,3 Explain memory reference instruction with examples. (15)**Q.4** Describe Stack organization in brief. (15)**Q.5** Explain DMA based data transfer technique for I/O devices. (15)Q.6. Discuss the structure of associative memory with help of a diagram. (15)**Q.7** Write a short note on any **TWO** of the following: (15)**RISC** a) Program loops **b**) Register transfer language c) **SECTION - II Q.8** Solve the following: (20)a) Find 2's complement of: (01010111)<sub>2</sub>  $(1010100)_2 - (1010100)_2$ b)  $(01011)_2*(101)_2$ c) d) Convert the expression in proper notation and solve it with stack: [(3+4)\*(7+2)]\*(9+4)Show that : A + A' B + A' B'=1**e**) **Q.9** What do you mean by sequential circuits? Explain any sequential circuit (20) with help of circuit diagram, State table and state diagram. Q.10a) Solve using Boolean algebra: (10)(BC' + A'D) (AB' + CD')i) ii) A'B + ABC' + ABCb) Solve using K-map: (10) $F(A, B, C) = \sum (0, 2, 3, 4, 6)$ i) ii)  $F(A,B,C,D) = \sum (0, 1, 2, 4, 5, 7, 11, 15)$